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PATENT ABSTRACTS OF JAPAN

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(71)Applicant: SUMITOMO WIRING SYST LTD

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(72)Inventor: FUKATSU YUKIHIRO

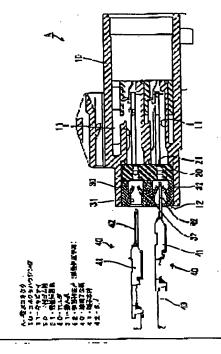
ICHIDA KIYOBUMI

(54) WATERPROOF CONNECTOR

(57)Abstract:

PROBLEM TO BE SOLVED: To correct the position and attitude of a tab of a male terminal fitting when the tab passes an insertion hole of a holder.

SOLUTION: The male terminal fitting 40 with the elongated tab 42 projected forward from a terminal body 41 is inserted into a cavity 11 in a connector housing 10 after sequentially passing the insertion hole 31 of the holder 30 and a wire insertion hole 21 of a bulk rubber plug 20. In the insertion hole 31, elastic pressing pieces 32 (elastic pressing means) for elastically pressing the tab 42 inserted into the insertion hole 31 in a direction intercrossing with the insertion direction is formed. The tab 42 inserted into the insertion hole 31 is positioned in the direction intercrossing with the insertion direction by the pressing of the pressing pieces 32, and corrected to a correct insertion attitude. The tab 42 having passed the insertion hole 42 never runs into the rubber plug 20.



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(21)Application number: 06-316556

(71)Applicant : OKI DATA:KK

OKI ELECTRIC IND CO LTD

(22)Date of filing:

20.12.1994

(72)Inventor: TEJIMA MINORU

NAKAMURA YUKIO

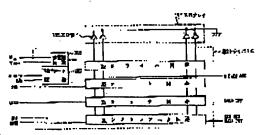
ISHIZAKI TAKASHI OTAKA KENGO

(54) LED HEAD DRIVING CIRCUIT

(57) Abstract:

PURPOSE: To eliminate the necessity of the step of cutting a polysilicon fuse at each driver IC and to obviate the necessity of replacing the IC.

CONSTITUTION: The LED head driving circuit comprises an LED array 12 having a plurality of LED elements 13, a shift register 15 for storing a print data signal, a latched circuit 17 for latching pixel data stored in the register 16, a gate circuit 18 for outputting a print information signal corresponding to the output of the circuit 17, a driver 19 for flashing the elements 13 corresponding to the information signal, a constant current circuit 103 for generating a constant current to be supplied to the driver 19, and a correcting gate circuit 102 connected to the circuit 103 and having a plurality of resistors. When the circuit 102 inputs correction data, the combined resistance value of the combination of the resistors is altered corresponding to the correction data to alter the constant current.



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